

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	5	(power adj sav\$3) near5 SDRAM	USPAT	OR	OFF	2006/02/01 11:49
S2	9	(power adj sav\$3) near5 SDRAM	US-PGPUB	OR	OFF	2006/02/01 11:52
S3	4	(power adj sav\$3) near5 SDRAM	EPO; JPO; IBM_TDB	OR	OFF	2006/02/01 11:52
S4	1249	SDRAM with time\$1	USPAT	OR	OFF	2006/02/01 11:59
S5	4	SDRAM with time\$1 same (power adj mode)	USPAT	OR	OFF	2006/02/01 11:59
S6	1	SDRAM with time\$1 same (power adj saving)	USPAT	OR	OFF	2006/02/01 12:05
S7	7	SDRAM with time\$1 same (power adj saving)	US-PGPUB	OR	OFF	2006/02/01 12:06
S8	1	SDRAM with time\$1 same (power adj saving)	EPO; JPO; IBM_TDB	OR	OFF	2006/02/01 12:06
S9	0	WAITI adj command	USPAT	OR	OFF	2006/02/01 12:31
S10	7	WAITI	USPAT	OR	OFF	2006/02/01 12:34
S11	5	WAITI	US-PGPUB	OR	OFF	2006/02/01 12:38
S12	2	WAITI	EPO; JPO; IBM_TDB	OR	OFF	2006/02/01 12:38
S13	164	(power adj mode) with (laspse period) adj time	USPAT	OR	OFF	2006/02/01 12:40
S14	9	(power adj mode) with (laspse period) adj time with (SDRAM RAM controller)	USPAT	OR	OFF	2006/02/01 12:44
S15	5	(power adj mode) with (laspse period) adj time with (SDRAM RAM controller)	US-PGPUB	OR	OFF	2006/02/01 12:44
S16	0	(power adj mode) with (laspse period) adj time with (SDRAM RAM controller)	EPO; JPO; IBM_TDB	OR	OFF	2006/02/01 12:44
S17	8	(power adj mode) with (laspse period) adj time with memory	USPAT	OR	OFF	2006/02/01 12:44
S18	1	"5262998".pn.	USPAT	OR	OFF	2006/02/03 12:14
S21	0	"0632463"	EPO	OR	OFF	2006/02/03 12:15
S22	1	"632463"	EPO	OR	OFF	2006/02/03 12:15
S23	0	(cpu processor) with detection adj (circuit unit) with SDRAM	USPAT	OR	OFF	2006/03/31 17:14
S24	253	(cpu processor) with detection adj (circuit unit) with controller	USPAT	OR	OFF	2006/03/31 17:15
S25	58	(cpu processor) with detection adj (circuit unit) with controller with power	USPAT	OR	OFF	2006/03/31 17:16
S26	4	(cpu processor) with detection adj (circuit unit) with bus with controller with power	USPAT	OR	OFF	2006/03/31 17:17

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S27	0	(cpu processor) with detection adj (circuit unit) with waiti	USPAT	OR	OFF	2006/03/31 17:17
S28	0	(cpu processor) with detection adj (circuit unit) with wait adj command	USPAT	OR	OFF	2006/03/31 17:17
S29	6	(cpu processor) with detection adj (circuit unit) with wait	USPAT	OR	OFF	2006/03/31 17:17
S30	160	memory near5 (power near2 (conserv\$5 consum\$5 reduc\$5 sav\$3)) with (delay\$3 wait\$3)	USPAT	OR	OFF	2006/03/31 17:33
S31	4	memory near5 (power near2 (conserv\$5 consum\$5 reduc\$5 sav\$3)) with (delay\$3 wait\$3) same cpu	USPAT	OR	OFF	2006/03/31 17:35
S32	1	memory near5 (power near2 (conserv\$5 consum\$5 reduc\$5 sav\$3)) with (delay\$3 wait\$3) near3 end	USPAT	OR	OFF	2006/03/31 17:33
S33	12	memory near5 (power near2 (conserv\$5 consum\$5 reduc\$5 sav\$3)) with (delay\$3 wait\$3) same processor	USPAT	OR	OFF	2006/03/31 17:35
S34	10	SDRAM near5 (power adj mode)	USPAT	OR	OFF	2006/04/03 09:59
S35	19	SDRAM near5 (power adj mode)	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2006/04/03 10:03
S36	190	(power adj mode) near2 after	USPAT	OR	OFF	2006/04/03 10:07
S37	47	(power adj mode) near2 after near5 time	USPAT	OR	OFF	2006/04/03 10:10
S38	2	(power adj mode) near2 after near5 time with memory	USPAT	OR	OFF	2006/04/03 10:07
S39	1	(power adj mode) near2 after near5 time with memory	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2006/04/03 10:10
S40	1	"5262998".pn.	USPAT	OR	OFF	2006/04/03 10:34
S41	10677	wait\$3 adj state	USPAT	OR	OFF	2006/04/03 12:56
S42	5	wait\$3 adj state with power adj mode	USPAT	OR	OFF	2006/04/03 12:56
S43	49	("5262998").URPN.	USPAT	OR	OFF	2006/04/03 18:12
S44	3	("4688196"   "4984216"   "4989183"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/03 18:27
S45	9	("6128747").URPN.	USPAT	OR	OFF	2006/04/03 18:29
S46	18	("5247655"   "5262998"   "5365487"   "5404543"   "5428252"   "5454114"   "5515539"   "5524249"   "5568637"   "5586308"   "5630090"   "5630143"   "5692197"   "5721935"   "5774736"   "5813022"   "5842029"   "5931951"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/03 18:40

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S47	3	("5568637").URPN.	USPAT	OR	OFF	2006/04/03 18:44
S48	4	("4249247"   "5276843"   "5369771"   "5371693").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/03 18:45
S49	300	mode adj transfer with memory	USPAT	OR	OFF	2006/04/03 18:49
S50	2	mode adj transfer with memory same set\$4 near2 register	USPAT	OR	OFF	2006/04/03 18:47
S51	39	mode adj transfer with memory same cpu	USPAT	OR	OFF	2006/04/03 18:49
S52	2	sdram near2 set\$4 adj register	USPAT	OR	OFF	2006/04/03 19:02
S53	108	mode adj transfer\$4 with enabl\$3	USPAT	OR	OFF	2006/04/03 19:02
S54	8447	impend\$4	USPAT	OR	OFF	2006/04/04 09:57
S55	49	impend\$4 near3 (cpu processor)	USPAT	OR	OFF	2006/04/04 10:11
S56	11	impend\$4 near3 (cpu processor) same memory	USPAT	OR	OFF	2006/04/04 09:58
S57	15	impend\$4 near3 (cpu processor) and power near2 (conserv\$5 consum\$5 reduc\$5 sav\$3)	USPAT	OR	OFF	2006/04/04 10:02
S58	1	impend\$4 near3 (cpu processor)	EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 10:11
S59	2284	(processor cpu) near3 set\$4 near3 mode	USPAT	OR	OFF	2006/04/04 10:16
S60	161	(processor cpu) near3 set\$4 near3 mode with power	USPAT	OR	OFF	2006/04/04 10:17
S61	29	(processor cpu) near3 set\$4 near3 mode with power with state	USPAT	OR	OFF	2006/04/04 10:17
S62	34	(processor cpu) near3 enabl\$3 near3 mode with power	USPAT	OR	OFF	2006/04/04 10:34
S63	0	(processor cpu) near3 enabl\$3 near3 mode with power	EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 10:34
S64	134	(set\$4 enabl\$3) near3 power adj saving adj mode	USPAT	OR	OFF	2006/04/04 10:37
S65	10	(processor cpu) near3 (set\$4 enabl\$3) near3 power adj saving adj mode	USPAT	OR	OFF	2006/04/04 10:37
S66	3	memory near3 power adj mode with after	USPAT	OR	OFF	2006/04/04 11:55
S67	1	memory near3 power adj mode with after	US-PGPUB	OR	OFF	2006/04/04 11:56
S68	1	memory near3 power adj mode with after	EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 11:56
S69	0	enabled adj setting with memory adj controller	USPAT	OR	OFF	2006/04/04 11:58
S70	386	enabled with memory adj controller	USPAT	OR	OFF	2006/04/04 11:58

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S71	0	enabled with memory adj controller with power adj mode	USPAT	OR	OFF	2006/04/04 11:59
S72	8	enabled with memory adj controller with power	USPAT	OR	OFF	2006/04/04 11:59
S73	2	last adj memory adj access and sleep	USPAT	OR	OFF	2006/04/04 12:55
S74	3	("6151262").URPN.	USPAT	OR	OFF	2006/04/04 15:46
S75	5	("5686767"   "5696729"   "5860016"   "5870613"   "5889723").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/04 15:49
S76	1	last adj memory adj access with (processor cput)	USPAT	OR	OFF	2006/04/04 15:54
S77	2	fetch adj detect\$3 adj (circuit unit device)	USPAT	OR	OFF	2006/04/04 15:59